

SYLLABUS

1. Data about the program of study

1.1 Institution	The Technical University of Cluj-Napoca
1.2 Faculty	Faculty of Automation and Computer Science
1.3 Department	Computer Science
1.4 Field of study	Computer Science and Information Technology
1.5 Cycle of study	Bachelor of Science
1.6 Program of study/Qualification	Computer science/ Engineer
1.7 Form of education	Full time
1.8 Subject code	44.

2. Data about the subject

2.1 Subject name	Computer networks				
2.2 Course responsible/lecturer	Prof. dr. eng. Vasile Dădârlat – vasile.dadarlat@cs.utcluj.ro				
2.3 Teachers in charge of seminars/ laboratory/ project	Assoc.prof. dr. eng. Peculea Adrian – Adrian.Peculea@cs.utcluj.ro Lect. dr. eng. Iancu Bogdan – Bogdan.Iancu@cs.utcluj.ro				
2.4 Year of study	IV	2.5 Semester	1	2.6 Type of assessment (E - exam, C - colloquium, V - verification)	E
2.7 Subject category	<i>DF – fundamentală, DD – în domeniu, DS – de specialitate, DC – complementară</i>				DD
	<i>DI – Impusă, DOp – opțională, DFac – facultativă</i>				DI

3. Estimated total time

3.1 Number of hours per week	4	of which:	Course	2	Seminars		Laboratory	2	Project	
3.2 Number of hours per semester	56	of which:	Course	28	Seminars		Laboratory	28	Project	
3.3 Individual study:										
(a) Manual, lecture material and notes, bibliography										40
(b) Supplementary study in the library, online and in the field										10
(c) Preparation for seminars/laboratory works, homework, reports, portfolios, essays										20
(d) Tutoring										
(e) Exams and tests										4
(f) Other activities:										
3.4 Total hours of individual study (suma (3.3(a)...3.3(f)))										74
3.5 Total hours per semester (3.2+3.4)										130
3.6 Number of credit points										5

4. Pre-requisites (where appropriate)

4.1 Curriculum	
4.2 Competence	Basic knowledge in programming languages (C, Java) Computer architecture, Operating systems

5. Requirements (where appropriate)

5.1. For the course	N/A
5.2. For the applications	Classroom, PC with internet access

6. Specific competence

6.1 Professional competences	<p>C2: Designing hardware, software and communication components</p> <p>C2.1: Describing the structure and functioning of computational, communication and software components and systems</p> <p>C2.2: Explaining the role, interaction and functioning of hardware, software and communication components</p> <p>C2.3: Building the hardware and software components of some computing systems using algorithms, design methods, protocols, languages, data structures, and technologies</p> <p>C2.4: Evaluating the functional and non-functional characteristics of the</p>
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	computing systems using specific metrics C2.5: Implementing hardware, software and communication systems
6.2 Cross competences	N/A

7. Discipline objective (as results from the key competences gained)

7.1 General objective	Teamwork, working with partial and contradicting specifications
7.2 Specific objectives	Each student able to design LAN's software & hardware architecture

8. Contents

8.1 Lectures	Hours	Teaching methods	Notes
Introduction. Concepts, network types, characteristics, evolution, standards	2	Oral Presentations using multimedia means Q & A Interactive teaching	
ISO-OSI Reference model and Internet's TCP/IP protocol stack. OSI abstract model presentation, description of protocol functions for every layer. General presentation for TCP/IP protocol stack	2		
Data transmission techniques. Data transmission concepts, analog and digital transmission techniques, coding, communication channels	2		
Types of computer networks. Architectures, evolution, topologies, physical parameters	2		
Physical level. Transmission media, characteristics, performances, connectors, structured cabling system	2		
Medium access control. Medium access techniques for local (wired and wireless) and wide area networks	2		
Data Link level. Functions, problems, protocols, case study: HDLC	2		
Local Area Computer Networks. Fundamentals, architectures, evolution	2		
Local Area Computer Networks. Systems, performances	2		
Computer Networks Interconnection. Devices for network interconnection; presentation of bridges, switches and routers	2		
Internet access. IP (+ ICMP), IPv6 (+IGMP) protocols. Address resolution protocol. Routing protocols	2		
Transport level protocols. TCP protocol; congestion control. TCP and UDP sockets	2		
General introduction to Internet applications. File transfer. Electronic mail, multimedia transmissions, network management	2		
General introduction to Internet applications. Security issues	2		
Bibliography			
1. V.Dadarlat, E.Cebuc - Retele Locale de Calculatoare - de la cablare la interconectare, Editura Albastra (Microinformatica), Cluj, 2006, ISBN 973-650-161-2			
2. W. Stallings, <i>Data and Computer Communications</i> ; Prentice Hall , 2005			
3. A. S. Tanenbaum, <i>Rețele de Calculatoare</i> ; Agora Press,2004			
8.2 Applications – Seminars/Laboratory/Project	Hours	Teaching methods	Notes
Lab presentation; Elements of the structured cabling system	2	Practical exercises Brief presentation of possible solutions Self testing programmes	
Network connection techniques	2		
Spanning tree protocol	2		
Copper based media and cabling with UTP	2		
Medium access methods	2		
Flow control protocols	2		
Protocol Inspector	2		
Optical Fiber and components	2		
Wireless access	2		
IP Addressing	2		
Network Inspector	2		
Network programming using sockets I	2		
Network programming using sockets II	2		
Lab exam	2		

Bibliography

1. Notes & lab notes available at: <ftp.utcluj.ro>

**Se vor preciza, după caz: tematica seminariilor, lucrările de laborator, tematica și etapele proiectului.*

9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

Course content is kept state of the art by using latest protocols and devices available on the market.

10. Evaluation

Activity type	Assessment criteria	Assessment methods	Weight in the final grade
Course	Interactivity and initial preparation, intermediary and final written examinations	Written exam (2,5 h).	70%
Seminar			
Laboratory	Quality of practical work, participation	Continuous assessment, final written colloquium	30%
Project			
Minimum standard of performance: Grade calculus: 30% laboratory + 70% final exam Conditions for participating in the final exam: Laboratory ≥ 5 Conditions for promotion: grade ≥ 5			

Course responsible
Prof. dr. eng. Vasile Dădârlat

Head of department
Prof.dr.eng. Rodica Potolea

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1.2 Faculty	Faculty of Automation and Computer Science
1.3 Department	Computer Science
1.4 Field of study	Computer Science and Information Technology
1.5 Cycle of study	Bachelor of Science
1.6 Program of study/Qualification	Computer science/ Engineer
1.7 Form of education	Full time
1.8 Subject code	45.

2. Data about the subject

2.1 Subject name	<i>Distributed systems</i>				
2.2 Course responsible/lecturer	Prof. dr. eng. Ioan Salomie – Ioan.Salomie@cs.utcluj.ro				
2.3 Teachers in charge of seminars/ laboratory/ project	Assoc.prof.dr.Eng. Tudor Cioara, Assoc.prof.dr. Eng. Ionut Anghel, As. Drd. Marcel Antal, As. Drd. Claudia Pop, As. Drd. Dorin Moldovan				
2.4 Year of study	IV	2.5 Semester	1	2.6 Type of assessment (E - exam, C - colloquium, V - verification)	E
2.7 Subject category	<i>DF – fundamentală, DD – în domeniu, DS – de specialitate, DC – complementară</i>				DS
	<i>DI – Impusă, DOp – opțională, DFac – facultativă</i>				DI

3. Estimated total time

3.1 Number of hours per week	5	of which:	Course	2	Seminars		Laboratory	2	Project	1
3.2 Number of hours per semester	70	of which:	Course	28	Seminars		Laboratory	28	Project	14
3.3 Individual study:										
(a) Manual, lecture material and notes, bibliography										18
(b) Supplementary study in the library, online and in the field										6
(c) Preparation for seminars/laboratory works, homework, reports, portfolios, essays										24
(d) Tutoring										
(e) Exams and tests										12
(f) Other activities:										
3.4 Total hours of individual study (suma (3.3(a)...3.3(f)))										60
3.5 Total hours per semester (3.2+3.4)										130
3.6 Number of credit points										5

4. Pre-requisites (where appropriate)

4.1 Curriculum	Computer networks
4.3 Competence	Ability to analyze and design a local network using simulators available

5. Requirements (where appropriate)

5.1. For the course	Whiteboard, projector, computer
5.2. For the applications	Computers, software specific

6. Specific competence

6.1 Professional competences	<p>C4 - Improving the performances of the hardware, software and communication systems (2 credits)</p> <p>C4.1 - Identifying and describing the defining elements of the performances of the hardware, software and communication systems</p> <p>C4.2 - Explaining the interaction of the factors that determine the performances of the hardware, software and communication systems</p> <p>C4.3 - Applying the fundamental methods and principles for increasing the performances of the hardware, software and communication systems</p> <p>C4.4 - Choosing the criteria and evaluation methods of the performances of the hardware, software and communication systems</p>
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	<p>C4.5 - Developing professional solutions for hardware, software and communication systems based on performance optimization</p> <p>C5 - Designing, managing the lifetime cycle, integrating and ensuring the integrity of hardware, software and communication systems (2 credits)</p> <p>C5.1 - Specifying the relevant criteria regarding the lifetime cycle, quality, security and the computing system's interaction with the environment and the human operator</p> <p>C5.2 - Using interdisciplinary knowledge for adapting the computing system to the specific requirements of the application field</p> <p>C5.3 - Using fundamental principles and methods for ensuring the security, the safety and ease of exploitation of the computing systems</p> <p>C5.4 - Proper utilization of the quality, safety and security standards in the field of information processing</p> <p>C5.5 - Creating a project including the problem's identification and analysis, its design and development, also proving an understanding of the basic quality requirements</p> <p>C6 - Designing intelligent systems (1 credit)</p> <p>C6.1 - Describing the components of intelligent systems</p> <p>C6.2 - Using domain-specific tools for explaining and understanding the functioning of intelligent systems</p> <p>C6.3 - Applying the fundamental methods and principles for specifying solutions for typical problems using intelligent systems</p> <p>C6.4 - Choosing the criteria and evaluation methods for the quality, performances and limitations of intelligent systems</p> <p>C6.5 - Developing and implementing professional projects for intelligent systems</p>
6.2 Cross competences	N/A

7. Discipline objective (as results from the *key competences gained*)

7.1 General objective	Capacity to analyse, develop and implement distributed software systems
7.2 Specific objectives	<ul style="list-style-type: none"> -Capacity of designing distributed systems at both architectural and components' level by using the main concepts and paradigms of the domain as well as the capacity of understanding the relationships of the domain with other computer science areas. -Capacity of identifying the main models, techniques and technologies that could be used in the design of distributed systems by considering a set of functional and non-functional specifications and constraints -Capacity of developing and using service based technologies for designing distributed systems -Capacity of using Java and .NET, Spring, Angular JS and other Web technologies for designing distributed systems

8. Contents

8.1 Lectures	Hours	Teaching methods	Notes
Introduction – Characterization of Distributed Systems	2	-Using modern multimedia teaching methods and direct access to internet; -Challenging questions during lecturers -Students are invited to collaborate in research projects -Personal assistance hours during the semester and before the exam	
Distributed System Models	2		
Non-Functional Requirements	2		
Inter-process Communication	2		
Indirect Communication	2		
Remote Procedure Call (RPC)	2		
Distributed Computation Model	2		
Time and Causality in Distributed Systems	2		
Global States and Snapshots	2		
Distributed Data Processing – Concepts , Reference Architectures	2		
Distributed Data Processing – Data Distribution Techniques	2		

Distributed Transactions	2		
Distributed Concurrency Control	2		
Overview of Google Distributed Technologies	2		
Bibliography			
1. G. Coulouris, J.Dollimore, T.Kindberg – Distributed Systems. Concepts and Design, Addison Wesley, 2005			
2. A. Tanenbaum, M. van Steen – Distributed Systems, Prentice Hall, 2002			
3. A.D. Kshemkalyan M.Singhal - Distributed Computing, Cambridge Press 2008			
4. Ioan Salomie, Tudor Cioara - Lecture Notes, Lab Notes Project Notes and Assignments http://www.coned.utcluj.ro/~salomie/DS_Lic			
8.2 Applications – Seminars/Laboratory/Project	Hours	Teaching methods	Notes
Intro to Lab Resources		- Pre-defined exercises and assignments -Using modern multimedia teaching methods and direct access to internet; -Students are invited to collaborate in research projects -Personal assistance hours during the semester and before the exam	
A1. Request-Reply Communication Paradigm			
P1. Spring MVC Framework			
A1.1 The Basics (examples and hands on)			
A1.2 Web application using Request - Reply			
A2. Remote Procedure Call (RPC)			
A2.1 The Basics (examples and hands on)			
A2.2 RPC application using distributed objects (Java RMI or .NET)			
P2. Angular JS			
A3. Asynchronous communication using messaging			
A3.1 The Basics (examples and hands on)			
A3.2. Asynchronous DS application using Java or .Net messaging frameworks			
A4. Service Oriented Distributed Systems – Application design using 3 services (SOA-Java, SOA-.Net and REST (Java or .Net))			
Evaluation			
Bibliography			
1. Ioan Salomie, Tudor Cioara, Ionut Anghel, Tudor Salomie – Distributed Computing and Systems – A practical Approach, Albastra Publ. House, 2008			
2. Ioan Salomie, Tudor Cioara - Lecture Notes, Lab Notes Project Notes and Assignments http://www.coned.utcluj.ro/~salomie/DS_Lic			

**Se vor preciza, după caz: tematica seminariilor, lucrările de laborator, tematica și etapele proiectului.*

9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

Distributed Systems is a subject of the domain "Computers and Information Technology". It teaches students about the development and implementing of distributed software systems. The content was developed based on the analysis of similar disciplines from other universities as well as based on the requirements of the IT employees. The content was also evaluated by Romanian governmental agencies CNEAA and ARACIS.

10. Evaluation

Activity type	Assessment criteria	Assessment methods	Weight in the final grade
Course	The level of assimilation of the knowledge about distributed systems, teacher during the course	Written Exam	50%
Seminar			
Laboratory	-Capacity of designing distributed systems at both architectural and components' level by using the main concepts and paradigms of the domain as well as the capacity of understanding the relationships of the domain with other computer science areas. -Capacity of identifying the main models, techniques and technologies that could be used in the design of	Assignments evaluation, Project evaluation	30% 20%
Project			

	distributed systems by considering a set of functional and non-functional specifications and constraints -Individual activity during course, lab and project -Presence		
<p>Minimum standard of performance: - To be able to design and implement distributed software systems. Grade calculus: 30% laboratory + 15% project + 55% final exam Conditions for participating in the final exam: Laboratory ≥ 5, Project ≥ 5 Handing over all laboratory assignments and obtain a minimum grade of 5 on each assignment; At least 11 laboratory presences. Conditions for promotion: final exam ≥ 5 Obtain a minimum grade of 5 for each category of exam questions (theory, technologies and problem)</p>			

Course responsible
 Prof. dr. eng. Ioan Salomie

Head of department
 Prof.dr.eng. Rodica Potolea

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1.2 Faculty	Faculty of Automation and Computer Science
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1.4 Field of study	Computer Science and Information Technology
1.5 Cycle of study	Bachelor of Science
1.6 Program of study/Qualification	Computer science/ Engineer
1.7 Form of education	Full time
1.8 Subject code	46.1

2. Data about the subject

2.1 Subject name	Input/Output Systems and Peripheral Devices				
2.2 Course responsible/lecturer	Prof. Dr. Eng. Zoltan Francisc Baruch – Zoltan.Baruch@cs.utcluj.ro				
2.3 Teachers in charge of seminars/ laboratory/ project	Prof. Dr. Eng. Zoltan Francisc Baruch – Zoltan.Baruch@cs.utcluj.ro Eng. Mihai Grigorescu – mihairigorescu13@gmail.com				
2.4 Year of study	IV	2.5 Semester	1	2.6 Type of assessment (E - exam, C - colloquium, V - verification)	E
2.7 Subject category	DF – fundamentală, DD – în domeniu, DS – de specialitate, DC – complementară				DS
	DI – Impusă, DOp – opțională, DFac – facultativă				DOp

3. Estimated total time

3.1 Number of hours per week	4	of which:	Course	2	Seminars		Laboratory	2	Project	
3.2 Number of hours per semester	56	of which:	Course	28	Seminars		Laboratory	28	Project	
3.3 Individual study:										
(a) Manual, lecture material and notes, bibliography										34
(b) Supplementary study in the library, online and in the field										12
(c) Preparation for seminars/laboratory works, homework, reports, portfolios, essays										18
(d) Tutoring										5
(e) Exams and tests										5
(f) Other activities:										0
3.4 Total hours of individual study (suma (3.3(a)...3.3(f)))										74
3.5 Total hours per semester (3.2+3.4)										130
3.6 Number of credit points										5

4. Pre-requisites (where appropriate)

4.1 Curriculum	Computer Programming, Computer Architecture
4.4 Competence	Competences of disciplines Computer Programming and Computer Architecture

5. Requirements (where appropriate)

5.1. For the course	Projector, computer
5.2. For the applications	Computers, the Microsoft Visual Studio programming environment

6. Specific competence

6.1 Professional competences	<p>C4 – Improving the performances of the hardware, software and communication systems (2 credits)</p> <p>C4.1 – Identifying and describing the defining elements of the performances of the hardware, software and communication systems</p> <p>C4.2 – Explaining the interaction of the factors that determine the performances of the hardware, software and communication systems</p> <p>C4.3 – Applying the fundamental methods and principles for increasing the performances of the hardware, software and communication systems</p> <p>C4.4 – Choosing the criteria and evaluation methods of the performances of the hardware, software and communication systems</p>
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	<p>C4.5 - Developing performance based professional solutions for hardware, software and communication systems</p> <p>C5 – Designing, managing the lifetime cycle, integrating and ensuring the integrity of hardware, software and communication systems (3 credits)</p> <p>C5.1 – Specifying the relevant criteria regarding the lifetime cycle, quality, security and the computing system’s interaction with the environment and the human operator</p> <p>C5.2 - Using interdisciplinary knowledge for adapting an information system to application domain requirements</p> <p>C5.3 – Using fundamental principles and methods for ensuring the security, the safety and ease of exploitation of the computing systems</p> <p>C5.4 - Adequate utilization of quality, safety and security standards in information processing</p> <p>C5.5 - Realization of a project including problem identification and analysis, design and development, while proving the understanding of the basic quality needs and requirements</p>
6.2 Cross competences	N/A

7. Discipline objective (as results from the *key competences gained*)

7.1 General objective	Knowledge of operation and performance parameters for input/output interfaces and peripheral devices; ability to communicate with controllers of peripheral devices
7.2 Specific objectives	<ul style="list-style-type: none"> - Using basic methods and principles for enhancing performance of computer systems - Designing input/output interfaces for connecting various devices to the computer - Designing and implementing in software input/output protocols - Writing system programs for controlling input/output interfaces

8. Contents

8.1 Lectures	Hours	Teaching methods	Notes
Introduction. Programmed I/O	2	- PowerPoint presentations - Questions, discussions	
Interrupt-Driven I/O. Direct Memory Access. I/O Processors	2		
Buses. Electrical Considerations. Synchronous and Asynchronous Buses. Bus Arbitration. VME Bus	2		
Local Buses. PCI Bus. PCI-X Bus. PCI Express Bus	2		
PCI Bus Variants for Personal Computers. PCI Bus Variants for Industrial Systems	2		
Serial Buses: I ² C; SPI; USB	2		
Mid-Term Exam	2		
Liquid Crystal Displays. Liquid Crystals. TN Technology. Addressing Methods. Backlighting	2		
Liquid Crystal Displays (cont.). Characteristics. VA Technology. IPS Technology	2		
Plasma Displays. Field Emission Displays. Organic LED Displays	2		
Graphics Adapters. Structure of a Graphics Adapter. Color Representation. Video Memory. Graphics Accelerators. 3D Accelerators	2		
Graphics Processing Units. Digital Interfaces for Monitors: DVI; HDMI; DisplayPort	2		
Optical Discs. Physical Medium. Data Organization and Encoding. The CD-ROM Drive. Types of Compact Discs	2		
DVD Discs. Blu-Ray Discs	2		
Bibliography			
1. Baruch, Z. F., Computer Input/Output Systems (in Romanian), Cartea Albastră, Cluj-Napoca, 2000, ISBN 973-9443-39-7.			
2. Rosch, Winn L., Hardware Bible, Sixth Edition, Que Publishing, 2003, ISBN 0-7897-2859-1.			
8.2 Applications – Seminars/Laboratory/Project	Hours	Teaching methods	Notes

The Serial Port (I)	2	- Additional explanations - Using a programming environment for the C language	
The Serial Port (II)	2		
The PCI Express Bus (I)	2		
The PCI Express Bus (II)	2		
The System Management Bus (I)	2		
The System Management Bus (II)	2		
The Universal Serial Bus (I)	2		
The Universal Serial Bus (II)	2		
Printers	2		
The SCSI Interface	2		
The ATA Interface (I)	2		
The ATA Interface (II)	2		
Compact Discs. The ATAPI Interface	2		
Laboratory Colloquy	2		
Bibliography			
1. Lecture slides and laboratory works at http://users.utcluj.ro/~baruch/en/pages/teaching/inputoutput-systems.php			

*Se vor preciza, după caz: tematica seminariilor, lucrările de laborator, tematica și etapele proiectului.

9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

The contents of the discipline has been corroborated with the contents of similar disciplines in the USA and Europe, as well as with chapters related to input/output systems of acknowledged manuals used in prestigious universities. The discipline has been evaluated by the ARACIS agency.

10. Evaluation

Activity type	Assessment criteria	Assessment methods	Weight in the final grade
Course	Understanding theoretical concepts of input/output systems and the principle of operation for peripheral devices	Written exam	70%
Seminar			
Laboratory	Ability to write communication programs with controllers of peripheral devices	Written evaluation	30%
Project			
Minimum standard of performance: Finishing at least one application in each laboratory session Grade calculus: 30% laboratory + 70% final exam Conditions for participating in the final exam: Laboratory ≥ 5 Conditions for promotion: final exam ≥ 5			

Course responsible
Prof. Dr. Eng. Zoltan Baruch

Head of department
Prof.dr.eng. Rodica Potolea

SYLLABUS

1. Data about the program of study

1.1 Institution	The Technical University of Cluj-Napoca
1.2 Faculty	Faculty of Automation and Computer Science
1.3 Department	Computer Science
1.4 Field of study	Computer Science and Information Technology
1.5 Cycle of study	Bachelor of Science
1.6 Program of study/Qualification	Computer science/ Engineer
1.7 Form of education	Full time
1.8 Subject code	46.2

2. Data about the subject

2.1 Subject name	<i>Parallel and Distributed Computing</i>				
2.2 Course responsible/lecturer	S.I.dr.ing. Anca Hangan – Anca.Hangan@cs.utcluj.ro				
2.3 Teachers in charge of seminars/ laboratory/ project	S.I.dr.ing. Anca Hangan – Anca.Hangan@cs.utcluj.ro				
2.4 Year of study	IV	2.5 Semester	1	2.6 Type of assessment (E - exam, C - colloquium, V - verification)	E
2.7 Subject category	<i>DF – fundamentală, DD – în domeniu, DS – de specialitate, DC – complementară</i>				DS
	<i>DI – Impusă, DOp – opțională, DFac – facultativă</i>				DOp

3. Estimated total time

3.1 Number of hours per week	4	of which:	Course	2	Seminars		Laboratory	2	Project	
3.2 Number of hours per semester	56	of which:	Course	28	Seminars		Laboratory	28	Project	
3.3 Individual study:										
(a) Manual, lecture material and notes, bibliography										28
(b) Supplementary study in the library, online and in the field										14
(c) Preparation for seminars/laboratory works, homework, reports, portfolios, essays										24
(d) Tutoring										4
(e) Exams and tests										4
(f) Other activities:										
3.4 Total hours of individual study (suma (3.3(a)...3.3(f)))										74
3.5 Total hours per semester (3.2+3.4)										130
3.6 Number of credit points										5

4. Pre-requisites (where appropriate)

4.1 Curriculum	Fundamental Algorithms, Fundamental programming techniques
4.5 Competence	

5. Requirements (where appropriate)

5.1. For the course	Whiteboard, projector, computer
5.2. For the applications	Computers, software specific

6. Specific competence

6.1 Professional competences	<p>C4 Improving the performances of the hardware, software and communication systems (2 credits)</p> <p>C4.1 Identifying and describing the defining elements of the performances of the hardware, software and communication systems</p> <p>C4.2 Explaining the interaction of the factors that determine the performances of hardware, software and communication systems</p> <p>C4.3 Applying fundamental methods and principles for increasing performance of hardware, software and communication systems</p> <p>C4.4 Choosing criteria and methods for performance evaluation of hardware, software and communication systems</p>
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	<p>C4.5 Developing professional solutions for hardware, software and communication systems based on performance optimization</p> <p>C5 Designing, managing the lifetime cycle, integrating and ensuring the integrity of hardware, software and communication systems (3 credits)</p> <p>C5.1 Specifying the relevant criteria regarding the lifetime cycle, quality, security and computing system's interaction with the environment and human operator</p> <p>C5.2 Using interdisciplinary knowledge for adapting the computing system to the specific requirements of the application field</p> <p>C5.3 Using fundamental principles and methods for security, reliability and usability assurance of computing systems</p> <p>C5.4 Adequate utilization of quality, safety and security standards in information processing</p> <p>C5.5 Creating a project including the problem's identification and analysis, its design and development, also proving an understanding of the basic quality requirements</p>
6.2 Cross competences	N/A

7. Discipline objective (as results from the *key competences gained*)

7.1 General objective	<ol style="list-style-type: none"> 1. Students become aware of differences and similarities between parallel and distributed computing so the students understand the boundaries of both domains. 2. Students become familiar with the principles of designing parallel programs. 3. Students become familiar with the main classes of distributed algorithms.
7.2 Specific objectives	<p>Parallel algorithms performance and scalability.</p> <p>Parallel algorithms design.</p> <p>Distributed algorithms: time synchronization, distributed mutual exclusion, causal ordering, leader election and snapshots.</p>

8. Contents

8.1 Lectures	Hours	Teaching methods	Notes
Introduction: goal, administrative issues, definition of parallel system and distributed systems.	2	Interactive lectures using PPT presentations, exercises (at whiteboard) and questions addressed to the students.	
Performance and scalability: metrics, scalability definition, Amdahl's law.	2		
Parallel algorithm design: parallelization process, data dependency.	2		
Parallel algorithm design: case study: ocean simulation.	2		
Parallel algorithm design: decomposition techniques, mapping techniques for load balancing.	2		
Interconnection networks: static interconnection networks (metrics, topologies), dynamic interconnection networks (buses, crossbars, multistage networks).	2		
Dense matrix algorithms: matrix-vector multiplication (1D partitioning and 2D partitioning, comparison 1D to 2D), matrix-matrix multiplication (2D partitioning, Cannon algorithm).	2		
Time: physical clocks synchronization (Cristian algorithm, Berkeley algorithm, Network Time Protocol), logical clocks (Scalar time, Vector time, efficient implementation of vector clocks - Singhal-Kshemkalyani).	2		
Distributed mutual exclusion: problem definition, Token-ring, Suzuki-Kasami, central coordinator, Lamport, Ricart-Agrawala.	2		
Causal ordering: problem definition, Birman-Schiper-Stephenson, Schiper-Eggli-Sandoz.	2		
Leader election: problem definition, general networks (FloodMax, OptFloodMax), synchronous / asynchronous ring (LeLann, Chang-Roberts, Hirschberg-Sinclair).	2		
Leader election: synchronous / asynchronous ring (Franklin, Peterson), anonymous ring (Itai-Rodeh).	2		

Snapshot: problem definition, Chandy-Lamport, Spezialetti-Kearns, Lai-Yang.	2		
Data analysis with Hadoop Discussion on parallel vs. distributed vs. concurrent.	2		
Bibliography			
1. <i>Parallel and Distributed Computing - Lecture notes</i> - C. Ivan , http://ftp.utcluj.ro/pub/users/civan/PDC			
2. <i>Concepte, mecanisme și soluții middleware pentru dezvoltarea sistemelor distribuite</i> , C.Ivan, Ed.Risoprint (CNCSIS), ISBN 987-973-53-1776-8, 2016			
3. <i>Arhitecturi paralele de calcul</i> , C. Ivan , Editura Roprint (CNCSIS) ,ISBN 973-354-23-4-1, 2001			
4. <i>Parallel Programming for Multicore and cluster systems</i> ,Rauber T, Runger. G, Springer Verlag			
5. ISBN 978-3-642-04817-3,2010			
6. <i>Introduction to Distributed Systems</i> -Concepts and design. George Coulouris, Jean Dollimore and Tim Kindberg, Prentice Hall, ISBN 0201-619-180, 2005 si editia revizuită 2008			
7. <i>Distributed computing : principles, algorithms and systems</i> , M. Singhal, A Kshemkalyani,Cambridge Univesrity Press, 0521876346 , 2008			
8.2 Applications – Seminars/Laboratory/Project	Hours	Teaching methods	Notes
Modern computing models. Concurrent vs. parallel vs distributed.	2	Problem based approach.	
Multithreading and concurrency in Java.	2		
Shared memory parallel programming.(OpenMP).Fundamental parallel algorithms implementation and analysis. (serch, graph, matrix)	2		
Shared memory parallel programming.(OpenMP).Fundamental parallel algorithms implementation and analysis.	2		
Message passing parallel programming (MPI). Fundamental parallel algorithms implementation and analysis.(serach, graph, matrix)	2		
Message passing parallel programming (MPI). Fundamental parallel algorithms implementation and analysis.	2		
Other parallel programming libraries (Pj2, PP –Net)	2		
Distributed programming – Sockets	2		
Distributed programming - Remote methods invocation	2		
Distributed algorithms- Logical clocks, leader election, distributed mutual exclusion.	2		
Group communication	2		
P2P systems	2		
Parallel processing in distributed context. Models, frameworks	2		
Project presentation/Laboratory test	2		
Bibliography			
1. <i>Parallel and Distributed Computing – Practical activities</i> - C. Ivan , http://ftp.utcluj.ro/pub/users/civan/PDC			
2. <i>Calcul paralel și distribuit</i> - Lucrari practice, C. Ivan ,Editura UTPress , ISBN ISBN 978-973-662-283-0, 2007			
3. <i>Introduction to Parallel Computing</i> , V.Kumar, A. Grama, A. Gupta, G. Karypis, Benjamin-Cummings,ISBN 0-201-64865-2			
4. <i>Programming on parallel machines</i> - GPU, multicore and clusters,N. Mathloff, Universityof California Davis, 2016 , http://heather.cs.ucdavis.edu/~matloff/158/PLN/ParProcBook.pdf			

*Se vor preciza, după caz: tematica seminariilor, lucrările de laborator, tematica și etapele proiectului.

9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

As Cluj software workforce market gets more sophisticated, having solid knowledge of how to develop parallel programs and mastering the distributed computing are qualities that software companies look for.

10. Evaluation

Activity type	Assessment criteria	Assessment methods	Weight in the final grade
Course	Formal assessment to test theoretical knowledge and problem solving skills. Attendance and activity.	Written exam.	40%
Seminar			
Laboratory	Formal assessment to test practical skills for	Project, Research report and	60%

	designing parallel and distributed solutions and implementation . Attendance and activity.	Homeworks during term.	
Project			
<p>Minimum standard of performance: Design and implementation of parallel/distributed solutions using the theoretical models and tools (OpenMP, MPI, PP.Net, Sockets,RMI). Pre-requisite for written exam: 6 mandatory lecture attendances. Grade calculus: 60% laboratory + 40% final exam Conditions for participating in the final exam: Laboratory \geq 5 Conditions for promotion: grade \geq 5</p>			

Course responsible
S.I.dr.ing. Anca Hangan

Head of department
Prof.dr.eng. Rodica Potolea

SYLLABUS

1. Data about the program of study

1.1 Institution	The Technical University of Cluj-Napoca
1.2 Faculty	Faculty of Automation and Computer Science
1.3 Department	Computer Science
1.4 Field of study	Computer Science and Information Technology
1.5 Cycle of study	Bachelor of Science
1.6 Program of study/Qualification	Computer science/ Engineer
1.7 Form of education	Full time
1.8 Subject code	47.1

2. Data about the subject

2.1 Subject name	Operating Systems Design				
2.2 Course responsible/lecturer	Assoc.prof. dr. eng. Adrian Coleșa – adrian.colesa@cs.utcluj.ro				
2.3 Teachers in charge of seminars/ laboratory/ project	Assoc.prof. dr. eng. Adrian Coleșa – adrian.colesa@cs.utcluj.ro Eng. Radu Ciocas – rciocas@bitdefender.com Eng. Alexandru Gurzou – agurzou@bitdefender.com Eng. Radu Portase – rportase@bitdefender.com Eng. Alexandru Brîndușe – abrinduse@bitdefender.com				
2.4 Year of study	IV	2.5 Semester	1	2.6 Type of assessment (E - exam, C - colloquium, V - verification)	E
2.7 Subject category	DF – fundamentală, DD – în domeniu, DS – de specialitate, DC – complementară				DS
	DI – Impusă, DOp – opțională, DFac – facultativă				DOP

3. Estimated total time

3.1 Number of hours per week	5	of which:	Course	2	Seminars		Laboratory	2	Project	1
3.2 Number of hours per semester	70	of which:	Course	28	Seminars		Laboratory	28	Project	14
3.3 Individual study:										
(a) Manual, lecture material and notes, bibliography										40
(b) Supplementary study in the library, online and in the field										0
(c) Preparation for seminars/laboratory works, homework, reports, portfolios, essays										42
(d) Tutoring										1
(e) Exams and tests										2
(f) Other activities:										0
3.4 Total hours of individual study (suma (3.3(a)...3.3(f)))							85			
3.5 Total hours per semester (3.2+3.4)							155			
3.6 Number of credit points							6			

4. Pre-requisites (where appropriate)

4.1 Curriculum	Operating Systems
4.6 Competence	C programming; Define and use basic OS concepts and system calls

5. Requirements (where appropriate)

5.1. For the course	Blackboard / Whiteboard, Beamer
5.2. For the applications	64-bit Computers with hardware virtualization support, 64-bit Linux and Windows, VMware Workstation, Blackboard / Whiteboard

6. Specific competence

6.1 Professional competences	<p>C5: Designing, managing the lifetime cycle, integrating and ensuring the integrity of hardware, software and communication systems</p> <p>C5.1: Specifying the relevant criteria regarding the lifetime cycle, quality, security and the computing system's interaction with the environment and the human operator</p> <p>C5.2: Using interdisciplinary knowledge for adapting the computing system to the</p>
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	<p>specific requirements of the application field</p> <p>C5.3: Using fundamental principles and methods for ensuring the security, the safety and ease of exploitation of the computing systems</p> <p>C5.4: Proper utilization of the quality, safety and security standards in the field of information processing</p> <p>C5.5: Creating a project including the problem's identification and analysis, its design and development, also proving an understanding of the basic quality requirements</p>
6.2 Cross competences	N/A

7. Discipline objective (as results from the *key competences gained*)

7.1 General objective	Provide the students a clear understanding of an OS' internal structure, its main components' role and functionality, and the fundamental OS design and implementation strategies.
7.2 Specific objectives	<p>Let the students:</p> <ol style="list-style-type: none"> 1. Know and understand the possible OS internal structures. 2. Know and understand the possible design and implementation alternatives of the main OS components, like the scheduler, process and thread manager, memory manager etc. 3. Be able to analyze a specific OS design problems and find solutions to them. 4. Be able to implement in C or assembly different OS components and system calls. 5. Be able to work in team and manage relatively complex software projects.

8. Contents

8.1 Lectures	Hours	Teaching methods	Notes
General structure of an OS. Possible OS structures (monolithic, layered, micro-kernel, virtual machine, exokernel), its components, their functionality, role, interconnectivity.	2	(1) use beamer slides, combined with blackboard illustration; (2) interactions with students: ask their opinion relative to the presented subject; (3) give each class a short evaluation test; let students discuss and argue each other their solution; give them the good solution and let them evaluate their own one; (4) propose 2-3 interesting study cases of OSES to be prepared and presented by students; (5) students are invited to collaborate in research projects.	
Process and thread management (1). Scheduling algorithms. FCFS, SJF, Priority-based, Lottery. Priority inversion.	2		
Process and thread management (2). Scheduling algorithms: RR, MLFQ. Use cases: Solaris, Windows and Linux scheduling policies.	2		
Synchronization mechanisms (1). General Design Principles. Hardware mechanisms used for implementation of higher-level synchronization mechanisms. Design and implementation of locks, semaphores, condition variables. Deadlock avoidance.	2		
Synchronization mechanisms (2). Linux and Windows Use Cases. The synchronization mechanisms provided by Linux and Windows. The way they are implemented.	2		
Synchronization mechanisms (3). Deadlock. Deadlock avoidance, prevention and detection algorithms.	2		
Process management (1). Definition of the process concept, system call mechanism and possible implementations, handle (file descriptor) management, basic system calls for process management.	2		
Process management (2). Process memory address space structure, argument passing on the stack, process creation strategies, multi-threading support.	2		
Memory management (1). General aspects, design and implementation alternatives of different memory management techniques and mechanisms: contiguous allocation, segmentation, and paging.	2		
Memory management (2). Paging specific problems like page table hierarchical structure, memory sharing, page tables for Intel architecture.	2		
Memory management (3). Virtual memory's design and	2		

implementation aspects: swapping and lazy loading. Page replacement algorithms.			
File systems (1). General Design Aspects. Design and implementation alternatives of file systems concepts (files, directories), storage space management. Advantages and disadvantages.	2		
File systems (2). Linux and Windows File Systems. Design and implementation of Ext2 and NTFS.	2		
Security aspect. Subject review. Basic security aspects design. Overview of all presented subjects.	2		
Bibliography			
1. A. Silberschatz, G. Gagne, P. B. Galvin, <i>Operating Systems Concepts</i> , 7 th edition, Wiley, 2005, ISBN 978-0-471-69466-3			
2. A. Tanenbaum, A. Woodhull. <i>Operating Systems Design and Implementation</i> . 3 rd edition, Prentice Hall, 2006, ISBN: 0131429388			
3. Daniel Pierre Bovet, <i>Understanding Linux Kernel</i> , O'Reilly & Associates, 2001, ISBN 0-596-00002-2.			
8.2 Applications – Seminars/Laboratory/Project	Hours	Teaching methods	Notes
Introduction. Presentation of the lab / project OS (Pintos or HAL9000).		(1) students are presented a very brief overview of the most important and difficult aspects of the working subject; (2) students are given at the beginning of each class a short evaluation quiz; (3) students are given a hands-on tutorial to practice with working subject's aspects and to solve problems (4) students are given challenging problems for extra credit;	
OS Debugging. Techniques and tools to debug an OS.			
<i>Thread management. Support for managing multiple executions inside the OS kernel.</i>			
<i>Synchronization mechanisms. Implementation of locks, semaphores and condition variables.</i>			
Scheduling algorithms. Round-Robin, priority-based, multi-level feedback queue (MLFQ).			
User application support (1). System call mechanism. Learn the way the system calls are implemented and used. Basic system call handling in the OS kernel.			
User application support (2). Basic memory management. Implementation of basic system calls.			
User application support (3). Multi-threaded application support.			
Virtual memory (1). Lazy-loading mechanisms.			
Virtual memory (2). Memory-mapped files.			
Virtual memory (3). Swapping and page-replacement algorithms.			
File system (1). Basic aspects of file implementation.			
File system (2). Basic aspects of directory implementation.			
Lab examination.			
Bibliography			
1. Lecture slides and laboratory text and support at http://moodle.cs.utcluj.ro/			
2. Pintos and HAL9000 manual.			

*Se vor preciza, după caz: tematica seminariilor, lucrările de laborator, tematica și etapele proiectului.

9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

OS knowledge is a fundamental requirement in the CS field. The OSD course presents techniques for hardware and software resources management, which are applicable on any complex management software application. Besides, it provides students detailed knowledge about modern OSes' internals, making them capable of developing more efficient applications. We follow the ACM curricula guide. OSD course's curriculum also maps the IT companies expectations, especially those dealing with direct access to OS services or developing kernel drivers or modules. Such companies are, for instance, system and data security and antivirus detection companies. Usually the teachers in charge of lab classes are former graduate students of our CS program with consistent experience in industry, in companies like those mentioned above. They are permanently consulted regarding the OS course curriculum and its applicability in real projects in industry.

10. Evaluation

Activity type	Assessment criteria	Assessment methods	Weight in the final grade
Course	Students must understand fundamental OS	Oral examination. Detailed	0.67

	structure and design alternatives and be able to explicitly describe it. They must also be able to apply their knowledge to give solutions to specific OS design problems.	discussion about design alternatives of different OS components.	
Seminar			
Laboratory	Students must be able to develop different OS components writing code in C and assembly.	<i>Lab</i> : implementation of different problems in the lab OS. <i>Project</i> : presentation of design and implementation solutions	0.33
Project			
<p>Minimum standard of performance:</p> <p>Students must attend minimum 9 lecture classes to be allowed to take the exam in the regular exam session. Students must attend minimum 7 lecture classes to be allowed to take the exam in any re-examination sessions. Less than 7 attended lecture classes leads to the interdiction to take any course re-examination in the university year the course is taught.</p> <p>Students must attend minimum 12 lab classes to be allowed to take the exam in the regular exam session. Students must attend minimum 10 lab classes to be allowed to take the exam in any re-examination sessions. Less than 10 attended lab classes leads to the interdiction to take any lab re-examination in the university year the course is taught.</p> <p>Students are allowed to take the final course examination only after passing the lab examination.</p> <p>Be able to describe the internal aspects of the fundamental OS design principles, like locks, priority-based and RR scheduling, system calls, paging, virtual memory.</p> <p>Be able to write functional C code that pass at least one test from the provided test set.</p>			

Course responsible
Assoc.prof.dr.eng. Adrian Colesa

Head of department
Prof.dr.eng. Rodica Potolea

SYLLABUS

1. Data about the program of study

1.1 Institution	The Technical University of Cluj-Napoca
1.2 Faculty	Faculty of Automation and Computer Science
1.3 Department	Computer Science
1.4 Field of study	Computer Science and Information Technology
1.5 Cycle of study	Bachelor of Science
1.6 Program of study/Qualification	Computer science/ Engineer
1.7 Form of education	Full time
1.8 Subject code	47.2

2. Data about the subject

2.1 Subject name	User Interface Design				
2.2 Course responsible/lecturer	Prof.dr.eng. Gorgan Dorian – dorian.gorgan@cs.utcluj.ro				
2.3 Teachers in charge of seminars/ laboratory/ project	S.I.dr.eng. Ștefănuț Teodor, teodor.stefanut@cs.utcluj.ro, S.I.dr.eng. Sabou Adrian, adrian.sabou@cs.utcluj.ro				
2.4 Year of study	IV	2.5 Semester	1	2.6 Type of assessment (E - exam, C - colloquium, V - verification)	E
2.7 Subject category	DF – fundamentală, DD – în domeniu, DS – de specialitate, DC – complementară				DS
	DI – Impusă, DOp – opțională, DFac – facultativă				DOp

3. Estimated total time

3.1 Number of hours per week	5	of which:	Course	2	Seminars		Laboratory	2	Project	1
3.2 Number of hours per semester	70	of which:	Course	28	Seminars		Laboratory	28	Project	14
3.3 Individual study:										
(a) Manual, lecture material and notes, bibliography										40
(b) Supplementary study in the library, online and in the field										10
(c) Preparation for seminars/laboratory works, homework, reports, portfolios, essays										20
(d) Tutoring										6
(e) Exams and tests										9
(f) Other activities:										0
3.4 Total hours of individual study (suma (3.3(a)...3.3(f)))							85			
3.5 Total hours per semester (3.2+3.4)							155			
3.6 Number of credit points							6			

4. Pre-requisites (where appropriate)

4.1 Curriculum	Computer programming (C or Java); Elements of Computer Assisted Graphics Software Engineering
4.7 Competence	The fundamental methodology for the development of software applications

5. Requirements (where appropriate)

5.1. For the course	Projector, computer
5.2. For the applications	Laboratory attendance is mandatory Study of laboratory materials from the server

6. Specific competence

6.1 Professional competences	<p>C5 - Designing, managing the lifetime cycle, integrating and ensuring the integrity of hardware, software and communication systems (6 credite)</p> <p>C5.1 - Specifying the relevant criteria regarding the lifetime cycle, quality, security and the computing system's interaction with the environment and the human operator</p> <p>C5.2 - Using interdisciplinary knowledge for adapting the computing system to the specific requirements of the application field</p> <p>C5.3 - Using fundamental principles and methods for ensuring the security, the</p>
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	<p>safety and ease of exploitation of the computing systems</p> <p>C5.4 - Proper utilization of the quality, safety and security standards in the field of information processing</p> <p>C5.5 - Creating a project including the problem's identification and analysis, its design and development, also proving an understanding of the basic quality requirements</p>
6.2 Cross competences	N/A

7. Discipline objective (as results from the key competences gained)

7.1 General objective	Study and experiment the methodology of interactive software applications development. Study Human-Computer interaction techniques.
7.2 Specific objectives	<ol style="list-style-type: none"> 1. Apply the user centered software development methodology 2. Study and experiment the techniques that are specific to the flexible methodology of the development of interactive applications and graphical user interfaces 3. Implementation of new and efficient human-computer interaction techniques 4. Usability evaluation in interactive applications

8. Contents

8.1 Lectures	Hours	Teaching methods	Notes
Introduction. History	2	New multimedia teaching approaches will be used in classes. The course is interactive and includes demonstrations that exemplify different user interaction techniques and the software development methodology.	During the semester and before each exam there are a few preparation hours planned.
User interface development concepts	2		
Input and output communication concepts	2		
User oriented design methodology	2		
User interface design methodology	2		
User interface usability	2		
User requirements definition	2		
Task description and analysis	2		
User interface prototyping	2		
Cognitive walkthrough and heuristic evaluation	2		
Interaction styles and techniques	2		
Web technologies. Audio and video technologies	2		
Wireless technologies	2		
User interface development environments	2		
Bibliography			
<ol style="list-style-type: none"> 1. Shneiderman B.: "Designing the User Interface. Strategies for Effective Human Computer Interaction", Addison-Wesley, 1992. 2. Galitz W.O.: "<i>The Essential Guide to User Interface Design</i>". John Wiley & Sons, 1997. 			
In virtual library			
1. Course resources, http://cgis.utcluj.ro/teaching/			
8.2 Applications – Seminars/Laboratory/Project	Hours	Teaching methods	Notes
Laboratory			
Best practice in UI development	2	Documentation and examples will be available to the students, prior to the laboratory classes, on a dedicated server. The students will work independently but will also be assisted by the teacher.	
Introduction into HTML	2		
Basic notions of CSS formatting	2		
User interaction through JavaScript	2		
Intermediate knowledge assessment	2		
Best practice in Mobile Applications development	2		
Introduction in Android	2		
UI layout best practices. List controls.	2		
UI elements for advanced user interactions	2		
Intermediate knowledge assessment	2		
Introduction in Windows Mobile	2		
UI layout best practices. List controls.	2		
UI elements for advanced user interaction	2		
Final knowledge assessment	2		

Project			
Project proposal: subject, methodology, phases, organization, project contents, project evaluation	1	Documentation and examples will be available to the students on a dedicated server.	Each student will have to develop a specific project based on the knowledge acquired at the laboratory hours.
Project definition. Evaluation report	1		
Task description and analysis	1		
Low fidelity prototyping, and scenarios	1		
Cognitive walkthrough	1		
Heuristic evaluation	1		
Prototyping plan	1		
Prototype codification	1		
User test cases	1		
Prototype evaluation and evaluation reports	1		
Iterative enhancement of the prototype	1		
Final user interface development	1		
Document writing	1		
Project presentation and evaluation	1		
Bibliography			
1. Teodor Ștefănuț, Dănuț Mihon, Victor Băcu, Dorian Gorgan. <i>Proiectarea interfețelor utilizator - Îndrumător de laborator</i> , Editura U.T. PRESS Cluj-Napoca, ISBN 978-606-737-068-3, http://biblioteca.utcluj.ro/ , 2015.			

9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

This discipline is integrated into the Computers and Information Technology domain. The content is classic, yet modern, and introduces to students the user centered methodology for the development of interactive software applications and graphical interfaces. The content of this discipline has been aligned with the information presented in similar disciplines from other major universities and companies from Romania, Europe and USA and has been evaluated by the authorized Romanian governmental agencies (CNEAA and ARACIS).

10. Evaluation

Activity type	Assessment criteria	Assessment methods	Weight in the final grade
Course	The written exam evaluates the understanding of the information presented in classes and the ability to apply this knowledge.	Evaluation is performed through written examination.	60% (E)
	The activity in class evaluates the active involvement of the students in the teaching process and their participation to the discussions, debates and other class activities during the entire semester.	Evaluation is performed through a very short tests.	10% (AC)
Laboratory	Laboratory assessment evaluates the practical abilities obtained by the students. Through project assignments the students have the opportunity to develop their skill in applying the notions, concepts and methods presented in class.	Evaluation is performed through written exam and project assessment.	25% (C)
Project			25% (P)
Minimum standard of performance: Graduation requirement: $M \geq 5$, final mark $M = 0.4 * E + 0.25 * C + 0.25 * P + 0.1 * AC$			

Course responsible
Prof.dr.eng. Dorian Gorgan

Head of department
Prof.dr.eng. Rodica Potolea

SYLLABUS

1. Data about the program of study

1.1 Institution	The Technical University of Cluj-Napoca
1.2 Faculty	Faculty of Automation and Computer Science
1.3 Department	Computer Science
1.4 Field of study	Computer Science and Information Technology
1.5 Cycle of study	Bachelor of Science
1.6 Program of study/Qualification	Computer science/ Engineer
1.7 Form of education	Full time
1.8 Subject code	48.1

2. Data about the subject

2.1 Subject name	Pattern Recognition Systems				
2.2 Course responsible/lecturer	Prof. dr. eng. Sergiu Nedevschi – Sergiu.Nedevschi@cs.utcluj.ro				
2.3 Teachers in charge of seminars/ laboratory/ project	Conf. dr. eng. Florin Oniga, Sef lucrari Raluca Brehar, Sef lucrari Ion Giosan{Florin.Oniga, Raluca.Brehar, Ion.Giosan}@cs.utcluj.ro				
2.4 Year of study	IV	2.5 Semester	1	2.6 Type of assessment (E - exam, C - colloquium, V - verification)	E
2.7 Subject category	<i>DF – fundamentală, DD – în domeniu, DS – de specialitate, DC – complementară</i>				DS
	<i>DI – Impusă, DOp – opțională, DFac – facultativă</i>				DOp

3. Estimated total time

3.1 Number of hours per week	5	of which:	Course	2	Seminars		Laboratory	2	Project	1
3.2 Number of hours per semester	70	of which:	Course	28	Seminars		Laboratory	28	Project	14
3.3 Individual study:										
(a) Manual, lecture material and notes, bibliography										28
(b) Supplementary study in the library, online and in the field										20
(c) Preparation for seminars/laboratory works, homework, reports, portfolios, essays										28
(d) Tutoring										4
(e) Exams and tests										5
(f) Other activities:										0
3.4 Total hours of individual study (suma (3.3(a)...3.3(f)))										85
3.5 Total hours per semester (3.2+3.4)										155
3.6 Number of credit points										6

4. Pre-requisites (where appropriate)

4.1 Curriculum	Image Processing
4.8 Competence	Computer programming, Data structures and algorithms, Probability Theory, Artificial Intelligence.

5. Requirements (where appropriate)

5.1. For the course	Blackboard, video projector, computer
5.2. For the applications	Workstations, specific software (Visual Studio, Diblook, OpenCV, Matlab)

6. Specific competence

6.1 Professional competences	<p>C4 – Improving the performances of the hardware, software and communication systems (2 credits)</p> <p>C4.1 - Identifying and describing the defining performance elements of hardware, software and communication systems</p> <p>C4.2 - Explaining the interaction of the factors that determine the performances of hardware, software and communication systems</p> <p>C4.3 - Applying fundamental methods and principles for increasing performance of hardware, software and communication systems</p> <p>C4.4 - Choosing criteria and methods for performance evaluation of hardware,</p>
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	<p>software and communication systems</p> <p>C4.5 - Developing performance based professional solutions for hardware, software and communication systems</p> <p>C5 – Designing, managing the lifetime cycle, integrating and ensuring the integrity of hardware, software and communication systems (2 credits)</p> <p>C5.1 - Specifying the relevant criteria regarding the lifetime cycle, quality, security and the computing system’s interaction with the environment and the human operator</p> <p>C5.2 - Using interdisciplinary knowledge for adapting the computing system to the specific requirements of the application field</p> <p>C5.3 - Using fundamental principles and methods for ensuring the security, the safety and ease of exploitation of the computing systems</p> <p>C5.4 - Adequate utilization of quality, safety and security standards in information processing</p> <p>C5.5 - Creating a project including the problem’s identification and analysis, its design and development, also proving an understanding of the basic quality requirements</p> <p>C6 – Designing intelligent systems (2 credits)</p> <p>C6.1 - Describing intelligent systems’ components</p> <p>C6.2 - Using domain-specific tools for explaining the operation of intelligent systems</p> <p>C6.3 - Applying fundamental methods and principles for specifying solutions for typical problems using intelligent systems</p> <p>C6.4 - Choosing criteria and methods for the evaluation of quality, performances and limitations of information systems</p> <p>C6.5 - Developing and implementing professional projects for intelligent systems</p>
6.2 Cross competences	N/A

7. Discipline objective (as results from the *key competences gained*)

7.1 General objective	Knowledge, understanding and use of concepts related to pattern recognition.
7.2 Specific objectives	<p>Knowledge, understanding and use of model-based pattern recognition methods using statistical approaches, linear discriminant methods, support vectors, and ensemble of classifiers.</p> <p>Knowledge, understanding and use of the specific operations of a pattern recognition system: data preprocessing, dimensional reduction, relevant feature selection, building the prediction model, selection of the optimum model, performance analysis.</p>

8. Contents

8.1 Lectures	Hours	Teaching methods	Notes
Introduction	2	Interactive teaching, using oral presentations supported by multimedia tools, consultations, involving students in research and development activities.	
Probability Review	2		
Bayesian Decision Theory 1	2		
Bayesian Decision Theory 2	2		
Bayesian Decision Theory Case Studies	2		
Maximum Likelihood Estimation	2		
Kernel Density Estimation	2		
K Nearest Neighbors Estimation	2		
Linear Discriminant Functions	2		
Kernel Methods	2		
Support Vector Machines	2		
Ensemble Methods	2		
Clustering methods	2		
Feature Selection and Performance Estimation	2		
Bibliography			
1. Richard O. Duda, Peter E. Hart , David G . Stork, "Pattern Clasification", <i>John Wiley and Sons</i> , 2001.			
2. C.M. Bishop, "Pattern Recognition and Machine Learning", <i>Springer</i> , 2006			
3. S. Theodoridis, K. Koutroumbas, "Pattern Recognition", 2-nd Edition, <i>Academic Press</i> , 2008.			

4. K. Murphy, "Machine Learning: A Probabilistic Perspective", The MIT Press, 2012				
8.2 Applications – Seminars/Laboratory/Project	Hours	Teaching methods	Notes	
Laboratory				
Introduction	2	Presentation using the blackboard and multimedia tools.		
Least Mean Squares Line Fitting	2			
RANSAC – fitting a line to a set of points	2			
Hough Transform for line detection	2			
Distance Transform (DT). Pattern Matching using DT	2			
Probability Density Estimation	2			
K-Means Clustering	2			
Principal Component Analysis	2			
K-Nearest Neighbor Classifier	2		Experiments and implementation using specific software tools (MS Visual Studio, Diblook)	
Naïve Bayes Classifier: Simple Digit Recognition Application	2			
Linear classifiers. Perceptron algorithm	2			
Adabost with Decision Stumps	2			
Support Vector Machine	2			
Lab Assessment	2	Evaluation of the design and implementation phases.		
Project				
Topic assignment (week 1, 2)	2			
Analyzes, specification and design (week 3,4)	2			
Presentation of the approach (week 5,6)	2			
Implementation (week 6,7,8,9,10); Intermediate presentation (week 9,10)	2			
Evaluation and optimization (week 11,12)	2			
Report elaboration (week 12,13)	2			
Final Presentation (week 13,14)	2			
Bibliography				
S. Nedevschi, "Lecture Notes", ftp.utcluj.ro/pub/users/nedevschi/SRF/				

9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

The subject is part of the Computer Science and Information Technology curriculum, its contents combining fundamental and practical aspects used in the field of pattern recognition. The subject content is correlated with the specific curricula of other Universities, in Romania and abroad, and is evaluated by government agencies (CNEAA and ARACIS). The subject's activities are meant to make the students familiar with the applications and the research directions of the image processing field, helped by the internationally renowned experience of the teachers.

10. Evaluation

Activity type	Assessment criteria	Assessment methods	Weight in the final grade
Course	Testing the theoretical knowledge acquired, and the practical abilities of problem solving.	Written exam	50%
Seminar			
Laboratory	Testing the practical abilities of designing and implementing solutions to specific problems. Attendance and activity.	Lab assessment, project assessment	50%
Project			
Minimum standard of performance: Modeling and implementation of solutions to specific engineering problems, using the domain's formal apparatus. Grade calculus: 25% laboratory + 25% project + 50% final exam Conditions for participating in the final exam: Laboratory ≥ 5, project ≥ 5 Conditions for promotion: grade ≥ 5			

Course responsible
Prof. dr. eng. Sergiu Nedevschi

Head of department
Prof.dr.eng. Rodica Potolea

SYLLABUS

1. Data about the program of study

1.1 Institution	The Technical University of Cluj-Napoca
1.2 Faculty	Faculty of Automation and Computer Science
1.3 Department	Computer Science
1.4 Field of study	Computer Science and Information Technology
1.5 Cycle of study	Bachelor of Science
1.6 Program of study/Qualification	Computer science/ Engineer
1.7 Form of education	Full time
1.8 Subject code	48.2

2. Data about the subject

2.1 Subject name	Translators design				
2.2 Course responsible/lecturer	Assoc.prof. dr. eng. Emil Șt. Chifu – emil.chifu@cs.utcluj.ro				
2.3 Teachers in charge of seminars/ laboratory/ project	Ing. Mihai Anton Cerghizan				
2.4 Year of study	IV	2.5 Semester	1	2.6 Type of assessment (E - exam, C - colloquium, V - verification)	E
2.7 Subject category	DF – fundamentală, DD – în domeniu, DS – de specialitate, DC – complementară				DS
	DI – Impusă, DOp – opțională, DFac – facultativă				DOp

3. Estimated total time

3.1 Number of hours per week	5	of which:	Course	2	Seminars		Laboratory	2	Project	1
3.2 Number of hours per semester	70	of which:	Course	28	Seminars		Laboratory	28	Project	14
3.3 Individual study:										
(a) Manual, lecture material and notes, bibliography										30
(b) Supplementary study in the library, online and in the field										15
(c) Preparation for seminars/laboratory works, homework, reports, portfolios, essays										27
(d) Tutoring										10
(e) Exams and tests										3
(f) Other activities:										0
3.4 Total hours of individual study (suma (3.3(a)...3.3(f)))							85			
3.5 Total hours per semester (3.2+3.4)							155			
3.6 Number of credit points							6			

4. Pre-requisites (where appropriate)

4.1 Curriculum	Formal Languages and Translators, Computer Programming, Data Structures and Algorithms
4.9 Competence	<ul style="list-style-type: none"> - Basic knowledge of programming and data structures (preferably in the C and Java languages) - Concepts of generative grammars and formal languages - To know the basic principles in the design of interpreters and translators for languages artificial - Basic knowledge of relational databases and web applications

5. Requirements (where appropriate)

5.1. For the course	N/A
5.2. For the applications	Computers, specific software

6. Specific competence

6.1 Professional competences	<p>C4 - Improving the performances of the hardware, software and communication systems (2 credits)</p> <p>C4.1 - Identifying and describing the defining elements of the performances of the hardware, software and communication systems</p>
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	<p>C4.2 - Explaining the interaction of the factors that determine the performances of the hardware, software and communication systems</p> <p>C4.3 - Applying the fundamental methods and principles for increasing the performances of the hardware, software and communication systems</p> <p>C4.4 - Choosing the criteria and evaluation methods of the performances of the hardware, software and communication systems</p> <p>C4.5 - Developing professional solutions for hardware, software and communication systems based on performance optimization</p> <p>C5 - Designing, managing the lifetime cycle, integrating and ensuring the integrity of hardware, software and communication systems (2 credits)</p> <p>C5.1 - Specifying the relevant criteria regarding the lifetime cycle, quality, security and the computing system's interaction with the environment and the human operator</p> <p>C5.2 - Using interdisciplinary knowledge for adapting the computing system to the specific requirements of the application field</p> <p>C5.3 - Using fundamental principles and methods for ensuring the security, the safety and ease of exploitation of the computing systems</p> <p>C5.4 - Proper utilization of the quality, safety and security standards in the field of information processing</p> <p>C5.5 - Creating a project including the problem's identification and analysis, its design and development, also proving an understanding of the basic quality requirements</p> <p>C6 - Designing intelligent systems (1 credit)</p> <p>C6.1 - Describing the components of intelligent systems</p> <p>C6.2 - Using domain-specific tools for explaining and understanding the functioning of intelligent systems</p> <p>C6.3 - Applying the fundamental methods and principles for specifying solutions for typical problems using intelligent systems</p> <p>C6.4 - Choosing the criteria and evaluation methods for the quality, performances and limitations of intelligent systems</p> <p>C6.5 - Developing and implementing professional projects for intelligent systems</p>
6.2 Cross competences	N/A

7. Discipline objective (as results from the *key competences gained*)

7.1 General objective	<ul style="list-style-type: none"> To know the phases of programming language translators: lexical analysis, syntactic analysis, and code generation. To master the tree structure representation of XML documents.
7.2 Specific objectives	<ul style="list-style-type: none"> To know the classes of languages for which efficient translators and interpreters can be implemented. To know the rules for processing typical statements for interpreters. To understand the difference between structure and presentation of documents. By using the Java language, to implement parsers of type SAX and DOM for XML documents containing DTD validation information. By using the Java language, to implement XML document transformers, based on XSLT transformations.

8. Contents

8.1 Lectures	Hours	Teaching methods	Notes
Descriptive tools: extended Backus-Naur form.	2	- The main ideas with multimedia techniques - Details and examples at the blackboard, in interaction with the students	
Regular grammars and finite automata: finite automata, state diagrams and regular expressions.	2		
Context-free grammars and pushdown automata: examples.	2		
Lexical analysis: modules and interfaces (decomposition of the grammar, lexical analyzer interface), construction of the lexical	2		

analyzer (state diagrams, reserved words method).		- There are consultation hours - Students are invited to collaborate in research projects		
LL parsers: the LL(1) parsing algorithm for extended BNF grammars.	2			
LL parsers: computation of FIRST and FOLLOW sets.	2			
LL parsers: examples of recursive-descent applications.	2			
Theoretical results concerning the LL(k) and LR(k) grammars.	2			
LR parsers: LR(0) states, SLR(1) grammars.	2			
LR parsers: LALR(1) grammars.	2			
LR parsers: the LALR(1) algorithm.	2			
LR parsers: shift-reduce transitions, chain production elimination.	2			
LR parsers: LR table compression.	2			
Basic concepts of attribute grammars.	2			
Bibliography				
8. W.M. Waite and G. Goos, Compiler Construction, Springer-Verlag, 1984.				
9. I.A. Leția and E.Șt. Chifu, Limbaje formale și translaatoare, Ed. Casa cărții de știință, 1998.				
10. A.V. Aho, R. Sethi, and J.D. Ullman, Compilers: Principles, Techniques and Tools, Addison-Wesley, 1986.				
8.2 Applications – Seminars/Laboratory/Project		Hours	Teaching methods	Notes
Laboratory				
W3C XML Recommendation version 1.0.	2	Brief presentation at the blackboard, examples and exercises implemented and tested on the computers, followed by homework for each topic		
Parsing XML documents (“well-formed”).	2			
XML document validation using DTD.	2			
XML document validation using XSD.	2			
W3C XPath Recommendation version 1.0.	2			
W3C XSLT Recommendation version 1.0.	2			
XSL-FO (XML Stylesheet Language - Formatting Objects) 1.1.	2			
XML usage for storing Microsoft Office 2007/2010 documents - Apache POI/XSSF 3.13.	2			
XML Data Binding using JAXB 2.0.	2			
W3C XQuery Recommendation 1.0, XPath & XSLT 2.0.	2			
XML document storage in databases.	2			
eXist-db XML native DBMS 2.2.	2			
XQuery 3.0 and Update extensions in eXist-db	2			
XRX (XForms + REST(ful) + XQuery) Web Application Architecture & Development in eXist-db.	2			
Project				
Building recursive-descent parsers from extended BNF grammars.	2	Brief presentation at the blackboard, implementing and testing the assignment on the computer		
Recursive-descent (RD) applications: expression evaluator.	2			
RD applications: interpreter for a language operating on binary trees.	2			
RD applications: interpreter for a language operating on lists.	2			
RD applications: interpreter for a language operating on matrices.	2			
RD applications: code generator for an imperative language.	2			
RD test.	2			
Bibliography				
1. W3C Recommendations (i.e. Standards) appropriate for each Topic.				
2. Teach Yourself XML in 21 days, Steven Holzner, SAMS Publishing, 2004.				
3. XML Pocket Reference, 3rd Edition, Simon St. Laurent, Michael Fitzgerald, O’Reilly Media, 2005.				
4. I.A. Leția, D. Marcu, B. Ungureanu, Procesoare de limbaje. Îndrumător de laborator, Universitatea Tehnică din Cluj-Napoca, 1995.				

*Se vor preciza, după caz: tematica seminariilor, lucrările de laborator, tematica și etapele proiectului.

9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

It is a specialty course in Computer Science, its syllabus being both classical and modern. It teaches the students with the principles of efficient design and implementation of interpreters and translators for artificial languages. The syllabus of the course has been discussed with other important universities and companies from Romania, Europe, and USA. This syllabus has been evaluated by Romanian governmental agencies (CNEAA and ARAIS).

10. Evaluation

Activity type	Assessment criteria	Assessment methods	Weight in the final grade
Course	- Problem-solving skills - Attendance, Activity	- Written exam	44%
Seminar			
Laboratory	- Problem-solving skills	- Assessment of the XML activity, homework, and written exam	35%
Project	- Attendance, Activity	- Assessment of the RD activity and test	21%
Minimum standard of performance: Modelling typical engineering problems using the domain specific formal apparatus. Grade calculus: 35% lab + 21% project + 44% final exam Conditions for participating in the final exam: Lab \geq 5 Conditions for promotion: grade \geq 5			

Course responsible
 Assoc.prof.dr.eng. Emil Chifu

Head of department
 Prof.dr.eng. Rodica Potolea

SYLLABUS

1. Data about the program of study

1.1 Institution	The Technical University of Cluj-Napoca
1.2 Faculty	Faculty of Automation and Computer Science
1.3 Department	Computer Science
1.4 Field of study	Computer Science and Information Technology
1.5 Cycle of study	Bachelor of Science
1.6 Program of study/Qualification	Computer science/ Engineer
1.7 Form of education	Full time
1.8 Subject code	49.1

2. Data about the subject

2.1 Subject name	Marketing				
2.2 Course responsible/lecturer	Lector dr. Veronica Maier – veronica.maier@enm.utcluj.ro				
2.3 Teachers in charge of seminars/ laboratory/ project					
2.4 Year of study	IV	2.5 Semester	1	2.6 Type of assessment (E - exam, C - colloquium, V - verification)	C
2.7 Subject category	DF – fundamentală, DD – în domeniu, DS – de specialitate, DC – complementară				DS
	DI – Impusă, DOp – opțională, DFac – facultativă				DOp

3. Estimated total time

3.1 Number of hours per week	2	of which:	Course	2	Seminars		Laboratory		Project	
3.2 Number of hours per semester	28	of which:	Course	28	Seminars		Laboratory		Project	
3.3 Individual study:										
(a) Manual, lecture material and notes, bibliography										14
(b) Supplementary study in the library, online and in the field										7
(c) Preparation for seminars/laboratory works, homework, reports, portfolios, essays										14
(d) Tutoring										9
(e) Exams and tests										4
(f) Other activities:										-
3.4 Total hours of individual study (suma (3.3(a)...3.3(f)))									48	
3.5 Total hours per semester (3.2+3.4)									76	
3.6 Number of credit points									3	

4. Pre-requisites (where appropriate)

4.1 Curriculum	Not the case
4.10 Competence	Not the case

5. Requirements (where appropriate)

5.1. For the course	The existence of multimedia equipment
5.2. For the applications	Not the case

6. Specific competence

6.1 Professional competences	<p>C5 – Designing, managing the lifetime cycle, integrating and ensuring the integrity of hardware, software and communication systems (2 credits)</p> <p>C5.1 – Specifying the relevant criteria regarding the lifetime cycle, quality, security and computing system's interaction with the environment and human operator</p> <p>C5.2 – Using interdisciplinary knowledge for adapting the computing system to the specific requirements of the application field</p> <p>C5.3 - Using fundamental principles and methods for ensuring the security, the safety and ease of exploitation of the computing systems</p> <p>C5.4 – Adequate utilization of quality, safety and security standards in</p>
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	information processing C5.5 – Realization of a project including problem identification and analysis, design and development, while proving the understanding of the basic quality needs and requirements
6.2 Cross competences	CT1 – Honorable, responsible, ethical behavior, in the spirit of the law, in order to ensure the professional reputation (1 credit)

7. Discipline objective (as results from the *key competences gained*)

7.1 General objective	Understand, assimilate and use of basic marketing concepts, principles and techniques
7.2 Specific objectives	Understand the marketing orientation, the components of the marketing mix, the methods and techniques for collecting and processing marketing data, the strategic analysis determining the competitive advantage, the market segmentation and the buying decision criteria.

8. Contents

8.1 Lectures	Hours	Teaching methods	Notes
Marketing role in big and small companies and in society: to contribute to company prosperity through creating a high value for the customers; to make the company responsible on the long range in front of community, society and environment. Marketing specific activities	2	multimedia presentation, interactivity by exemplifying the presented concepts, using the questions-answer method during the course, discussing case studies, playing thematic strategy game, interactive lectures	
Marketing concepts (philosophies) in contemporary organizations: Volume? Quality? Sales? Customer satisfaction?	2		
Marketing environment analysis. Micro and macro environment: suppliers, interest groups, customers, economic, demographic, technological, natural, legal and cultural environment	2		
Marketing research: research plan, data collection; data analysis quantitative and qualitative techniques; experiments; research report. Marketing information systems	2		
Marketing strategic planning: creating and maintaining the balance between objectives, resources and market opportunities. Methods of strategic analysis.	2		
Designing the strategic plan at four levels: company, divisions, strategic units and brands	2		
Consumer behavior analysis: patterns of behavior	2		
Buying decision process	2		
Market segmentation. Criteria and methods of market segmentation	2		
Product policy. Product life cycle. Researching and developing new products	2		
Product strategies for the life cycle stages. Positioning strategies	2		
Pricing. Pricing policy objectives. Pricing and legal constraints. Pricing policies: market penetration and market skimming	2		
Product distribution. Choosing the distribution channels. Managing and controlling the distribution channels	2		
Marketing communication. Communication process. Marketing communication mix: advertising,	2		
Bibliography			
<ol style="list-style-type: none"> 1. D. Catana, Gh. A. Catana, Fundamentals of Marketing, UTPRES, 2009 2. Gh. A. Catana, A. Dobra Constantinescu, Marketing in powerpoint, UTPRES, 2004 3. Gheorghe Alexandru Catană, <i>Marketing: filozofia succesului de piață</i>, vol. I, Editura Dacia, Cluj-Napoca 2003 			
Virtual materials			
1. D. Catana, Gh. A. Catana, Marketing, 2010 www.marketing.utcluj.ro			
8.2 Applications – Seminars/Laboratory/Project	Hours	Teaching methods	Notes
-			
Bibliography			

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**Se vor preciza, după caz: tematica seminariilor, lucrările de laborator, tematica și etapele proiectului.*

9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

In order to provide the content for the lecture and establish the method of the teaching / learning process the professor organizes meetings with marketers, who speak to students about the needs and expectations of employers in the area.

10. Evaluation

Activity type	Assessment criteria	Assessment methods	Weight in the final grade
Course	The students answer to open ended and closed questions; involvement during the course by preparing and presenting teamwork papers.	Written exam	100%
Seminar			
Laboratory			
Project			
Minimum standard of performance: Requirement for the credits: N>5			

Course responsible
Assist. Prof. Veronica Maier, PhD

Head of department
Prof.dr.eng. Rodica Potolea

SYLLABUS

1. Data about the program of study

1.1 Institution	The Technical University of Cluj-Napoca
1.2 Faculty	Faculty of Automation and Computer Science
1.3 Department	Computer Science
1.4 Field of study	Computer Science and Information Technology
1.5 Cycle of study	Bachelor of Science
1.6 Program of study/Qualification	Computer science/ Engineer
1.7 Form of education	Full time
1.8 Subject code	49.2

2. Data about the subject

2.1 Subject name	Personal and professional development				
2.2 Course responsible/lecturer	Dipl. Psy. Dorin Stanciu PhD, Lecturer - ionut.stanciu@dppd.utcluj.ro				
2.3 Teachers in charge of seminars/ laboratory/ project	-				
2.4 Year of study	IV	2.5 Semester	1	2.6 Type of assessment (E - exam, C - colloquium, V - verification)	C
2.7 Subject category	DF – fundamentală, DD – în domeniu, DS – de specialitate, DC – complementară				DS
	DI – Impusă, DOp – opțională, DFac – facultativă				DOp

3. Estimated total time

3.1 Number of hours per week	2	of which:	Course	2	Seminars		Laboratory		Project	
3.2 Number of hours per semester	28	of which:	Course	28	Seminars		Laboratory		Project	
3.3 Individual study:										
(a) Manual, lecture material and notes, bibliography										16
(b) Supplementary study in the library, online and in the field										14
(c) Preparation for seminars/laboratory works, homework, reports, portfolios, essays										14
(d) Tutoring										-
(e) Exams and tests										4
(f) Other activities:										-
3.4 Total hours of individual study (suma (3.3(a)...3.3(f)))							48			
3.5 Total hours per semester (3.2+3.4)							76			
3.6 Number of credit points							3			

4. Pre-requisites (where appropriate)

4.1 Curriculum	-
4.1.1 Competence	Baccalaureate level of instruction and general knowledge Beginner literacy with desktop applications, including Internet utilization

5. Requirements (where appropriate)

5.1. For the course	Auditorium or large lecture room. Audio-video installation for on-screen presentations (with room speakers). WiFi or cable Internet connectivity.
5.2. For the applications	Auditorium or large lecture room. Audio-video installation for on-screen presentations (with room speakers). WiFi or cable Internet connectivity. Writing board (classical or interactive) / Flip chart

6. Specific competence

6.1 Professional competences	<p>C5 - Design, lifecycle management, integration and integrity of hardware, software and communication systems (2 credits)</p> <p>C5.1 - Specifying the relevant criteria regarding the lifetime cycle, quality, security and computing system's interaction with the environment and human operator</p> <p>C5.2 - Using interdisciplinary knowledge for adapting an information system to</p>
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	<p>application domain requirements</p> <p>C5.3 - Using fundamental principles and methods for security, reliability and usability assurance of computing systems</p> <p>C5.4 - Adequate utilization of quality, safety and security standards in information processing</p> <p>C5.5 - Realization of a project including problem identification and analysis, design and development, while proving the understanding of the basic quality needs and requirements</p>
6.2 Cross competences	TC1 - Honorable, responsible, ethical behavior, in the spirit of the law, to ensure the professional reputation (1 credit)

7. Discipline objective (as results from the *key competences gained*)

7.1 General objective	To provide the course graduate with a set of competencies, skills and level of knowledge about him/herself which allows the formation of a competitive advantage and to provide to course graduate with a better understanding of his/her current academic status and curriculum
7.2 Specific objectives	<p>To facilitate domain-specific learning and knowledge acquisition by providing a larger perspective on personal and professional development;</p> <p>To enhance personal determination and academic engagement as a basis for future competitiveness;</p> <p>To allow the course graduate to acquire specific tools and skills needed for personal and professional assessment, engagement, planning, organizing, expression, and networking.</p>

8. Contents

8.1 Lectures	Hours	Teaching methods	Notes
Key-concepts of personal and professional development. Self-concepts, qualities and virtues, self-awareness and self-knowledge (self-assessment versus external evaluations)	2	Interactive lectures: - Exposition - Discourse - Debating - Case studies - Problem-solving - Heuristic conversations - Role playing	
Motivation and self-determination. Goals, objectives, interests, needs, desires, ideals, aspirations, expectations and incentives.	2		
Learning and learning styles. Self-directed learning, adult learning and continuous learning (lifelong learning)	2		
Social modelling and key-persons/models. The basics of social learning and the significant others	2		
Rationality, control, self-regulation and decision making. Processes, strategies and decision making tools	2		
Critical thinking and scientific reasoning. Cognitive biases, logical fallacies and cognitive distortions	2		
Assertive communication, persuasion and negotiation	2		
Significant personal objectives: Qualities and virtues	2		
Significant personal and professional objectives: Health, safety, fulfilment, satisfaction and happiness	2		
Tools, means and methods for productivity enhancement: Strategic planning, Decision-making, Information management	2		
Tools, means and methods for productivity enhancement: Organization, scheduling, planning and budget management	2		
Tools, means and methods for productivity enhancement: CV building (principles, alternatives, instruments)	2		
Tools, means and methods for productivity enhancement: Social networking, Professional networking (virtual dedicated networks and communities)	2		
Personal and professional development plans. Design and presentation	2		
<p>Bibliography</p> <p>Study materials: Course synthesis, Lecture presentations, additional multimedia presentations)</p> <p>Aggarwal, R. S. (2000). <i>A Modern Approach to Verbal & Non Verbal Reasoning</i>: S.Chand.</p> <p>Andrews, K. R. (1971). <i>The concept of corporate strategy</i>: Dow Jones-Irwin.</p>			

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8.2 Applications – Seminars/Laboratory/Project	Hours	Teaching methods	Notes
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Bibliography

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**Se vor preciza, după caz: tematica seminariilor, lucrările de laborator, tematica și etapele proiectului.*

9. Bridging course contents with the expectations of the representatives of the community, professional associations and employers in the field

By completing this course, the course graduate is expected to have acquired a series of specific and general declarative and procedural knowledge, as well as have built a series of competencies, which, in their togetherness contribute to an increased capacity to find employment, communicate professionally and informally, collaborate and work closely with other professionals and non-professional, and an increased ability to promote and capitalize upon personal and professional traits and activities.

10. Evaluation

Activity type	Assessment criteria	Assessment methods	Weight in the final grade
Course	Standardized written test with multiple choice questions. Collaborative and individual semester projects. Collaborative and individual homework. Assessment criteria include: accuracy/precision, completeness, fluency, and relevance	Written test Duration: 1 hr. Individual portfolio	60% 40%
Seminar			
Laboratory			
Project			

Minimum standard of performance:
The total weighed score exceeds the equivalent of 5/10 of the final grade.
Each assessment exceeds 50% of the allotted grading.

Course responsible
DPsy. Dorin Stanciu PhD, Lecturer

Head of department
Prof.dr.eng. Rodica Potolea